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Professional Summary:

Hard-working computer engineering student with five years of schooling and personal experience. Detail oriented as many current projects require research and critical thought to ensure proper operation of the final design. Experienced in following customer requirements due to participation in the Northrop Gruman Collaboration Project.

Experience:

2022 - Present

California Polytechnic State University

Northrop Gruman Collaboration Project – (UGV Electrical Team)

- Working together as a team to create an unmanned ground vehicle, which can communicate with a control tower and other unmanned air vehicles to model the rescue of a downed hiker without human intervention.
- Working through all steps of a professional project from design and review to final testing.
- Adapting to issues and solving problems that may arise with the vehicles design.
- Working with data analysis tools, such as trade matrices, as well as design tools for electrical engineering.

2023 - Present

California Polytechnic State University

Orange County Sustainability Decathlon

- Responsible for designing the networking and lighting layout of a 1008 sq ft solar powered home.
- Choosing fixtures and appliances for best possible energy efficiency within the home.
- Working on home integration system to show homeowner power usage throughout the home, available power from panels stored in batteries, and allow control of appliances and lights.

School History:

2017 – 2019 **Mt. San Antonio College** GPA: 3.84

Undergraduate for transfer

2019 – Present California Polytechnic State University Pomona GPA: 3.95

Working towards a BS in Computer engineering

- ECE 3301/L Microcontrollers course in which I learned how to code different devices in order to get information from different sensor types. Gained an understanding of the interaction between the real world and electronics.
- ECE 3310 Data structures and algorithms course in which I learned many useful algorithms for database sorting and searching. Taught in C++.

Skills:

Circuit design/analysis Troubleshooting

Problem solving Experience with OrCAD

• Experience with Altium C++/C/C#/VHDL Program Languages